

Listing and/or Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A signal processing apparatus, comprising:

 a signal input for receiving an analog signal having a synchronizing characteristic;
 a first clock generator for generating a reference clock signal, the reference clock signal being independent of the synchronizing characteristic of the analog signal;

 a second clock generator, coupled to the first clock generator, for producing a plurality of further clock signals in response to the reference clock signal; and

 a signal processing section, coupled to the signal input and the second clock generator, for sampling and processing the analog signal in accordance with a sampling rate and an appropriate signal standard, the signal processing section having a plurality of analog to digital (A/D) converters that are clocked by respective ones of the plurality of further clock signals, ~~wherein~~

 the A/D converters ~~are~~ being clocked by respective ones of the plurality of further clock signals, which are independent of the synchronizing characteristic of the input analog signal and have a frequency substantially equal to the sampling rate, whereby corruption of the analog signal by digital noise in the apparatus is prevented.

2. (Previously Presented) The signal processing apparatus of claim 1, wherein the signal processing section is adapted to process two separate analog signals, each of the two separate analog signals having respective synchronizing characteristics, and the reference clock signal is independent of both of the synchronizing characteristics.

3. (Previously Presented) The signal processing apparatus of claim 2, wherein the signal processing section processes the two separate analog signals using a single processing channel, the single processing channel being clocked by a further clock signal that has a signal frequency of at least twice the required clocking speed necessary for processing a single one of the analog signals.

4. (Previously Presented) The signal processing apparatus of claim 3, wherein the analog signals are television signals.

5. (Previously Presented) The signal processing apparatus of claim 1, wherein the signal processing section is further operable to process a digital input signal having a synchronizing characteristic, and the reference clock signal is independent of the synchronizing characteristic of the digital input signal.

6. (Currently Amended) A television apparatus, comprising:

- a signal input for receiving a television signal having a synchronizing characteristic;

- a first clock signal generator for producing a reference clock signal that is independent of the synchronizing characteristic of the television signal;

- a second clock signal generator, coupled to the first clock signal generator, for producing a plurality of further clock signals in response to the reference clock signal;

- a signal processor, coupled to the signal input and the second clock signal generator, for sampling and processing the input signal in accordance with an appropriate signal standard and providing an output signal suitable for display on a display device, the signal processor including a plurality of analog to digital (A/D) converters coupled to the second clock signal generator; and

- an signal output, coupled to the signal processor, for receiving and coupling the output signal to a display device, ~~wherein~~

- the A/D converters of the signal processor ~~are~~ being clocked by respective ones of the plurality of further clock signals, which are independent of the synchronizing characteristic of the input signal, and have having a frequency substantially equal to the sampling rate whereby corruption of the analog signal by digital noise in the apparatus is prevented.

7. (Previously Presented) The television apparatus of claim 6, wherein the signal processing section is adapted to process two separate analog signals, each of the two

separate analog signals having respective synchronizing characteristics, and the reference clock signal is independent of the synchronizing characteristics.

8. (Previously Presented) The television apparatus of claim 7, wherein the analog signal processing section processes the two separate analog signals using a single processing channel, and the single processing channel is clocked by an internal clock signal that has a signal frequency of at least twice the required clocking speed necessary for processing a single one of the analog signals.

9. (Currently Amended) A method for processing input signals having synchronizing components, the method comprising the steps of:

- receiving an input signal having a synchronizing component;
- generating a reference clock signal, the reference clock signal being independent of the synchronizing characteristic of the input signal;
- generating a plurality of further clock signals based on the reference clock signal;
- converting the analog input signal into a digital signal using analog to digital (A/D) converters that are clocked using one of the plurality of further clock signals; and
- decoding the converted digital signal in accordance with an appropriate television signal standard using decoding circuitry/logic to provide an output signal suitable for display, the decoding circuitry/logic being clocked by at least one of the plurality of further clock signals, ~~wherein~~

- the A/D converters and the decoding circuitry/logic ~~are~~ being clocked by respective ones of the plurality of further clock signals, which are independent of the synchronizing characteristic of the input signal and have having a frequency substantially equal to the sampling rate whereby corruption of the analog signal by digital noise is prevented.

10. (Previously Presented) The method according to claim 9, wherein

- the receiving step comprises receiving two analog input signals, each having a respective synchronizing characteristic,

the converting step comprises converting the two analog input signals to respective digital signals, and

the decoding step comprises decoding the two digital signals to provide two output signal using a single processing channel that is clocked by an internal clock signals that has a frequency of at least twice the required clocking speed necessary for processing a single analog signal.

11. (Previously Presented) The method according to claim 9, wherein

the receiving step further comprises receiving a digital input signal having a synchronizing characteristic, and

the decoding step further comprises decoding the digital input signal using decoding circuitry/logic that is clocked by a respective one of the internal clock signals that is independent of the synchronizing characteristic of the digital input signal.

12. (Previously Presented) The method according to claim 9, wherein

the receiving step comprises receiving an analog television signal.